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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	09/499,619
Filing Date	February 7, 2000
First Named Inventor	Shunpei YAMAZAKI
Group Art Unit	2814
Examiner Name	N. Ngo
Attorney Docket Number	0756-2095

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴
		Office ²	Number ⁴	Kind Code ³ (if known)				
NW		JP	58-088787			05/26/1983		Full
I		JP	54-154992			12/16/1979		Full
I		JP	64-010711			01/13/1989		Abst.
NW		JP	01-236731			09/21/1989		Abst.
		JP	02-067817			03/07/1990		Abst.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
NW		H. YANAI, 3.3.2 <i>Basic Circuit of Analog Switch</i> , Semiconductor Handbook, 2 nd Edition, Pages 842-843, 1977.	Concise Statement

Examiner Signature	<i>N. Ngo</i>	Date Considered	10-13-05
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